# On-the-Fly Verification using CADP

Radu Mateescu

INRIA Rhône-Alpes / VASY, 655, avenue de l'Europe F-38330 Montbonnot Saint Martin, France Radu.Mateescu@inria.fr

#### Abstract

On-the-fly verification consists in analysing the correctness of a finite-state concurrent system by constructing and exploring its state space incrementally. This provides a way to fight against state explosion, by enabling the detection of errors in systems with large state spaces. We give an overview of the latest on-the-fly verification techniques developed within the CADP protocol engineering toolbox. These techniques are based upon on-the-fly resolution of (alternation-free) boolean equation systems, implemented in a generic software library named CÆSAR\_SOLVE, which is currently used for equivalence checking and model checking.

## 1 Introduction

A successful approach for automatically analysing the correctness of a concurrent finitestate system consists in building its state space and verifying that it meets an abstract specification, expressed either as an external behaviour (*equivalence checking*), or as a set of temporal logic properties (*model checking*). The main drawback of this approach is *state explosion* (prohibitive size of the state space), which occurs for systems containing many parallel processes and complex data structures. *On-the-fly* verification combats state explosion by constructing the state space in a demand-driven way, therefore enabling the detection of errors in large systems.

Building robust tools for on-the-fly verification is a complex and costly activity. This effort could be significantly reduced by using generic software components from which elaborated verification tools can be constructed. A successful attempt in achieving genericity was made by developing the OPEN/CÆSAR [5] environment, which is a central piece of the CADP verification toolbox [6]. OPEN/CÆSAR precisely defines an application programming interface (API) which represents state spaces or labelled transition systems (LTSs) in an *implicit* manner, by means of their "successor" function. This API allows to build generic verification tools that explore the LTS of a concurrent program on-the-fly, independently from the source language in which the program was written. Such verification tools are directly available for any language equipped with a compiler implementing a translation to the API of OPEN/CÆSAR. CADP currently contains several verification tools built using

OPEN/CÆSAR: EXECUTOR (random execution), EXHIBITOR (searching of regular transition sequences), OCIS (interactive and guided simulation), EVALUATOR (model checking of regular alternation-free  $\mu$ -calculus), etc.

Continuing the ideas underlying OPEN/CÆSAR, we developed a generic software library, called CÆSAR\_SOLVE, dedicated to on-the-fly resolution of alternation-free boolean equation systems (BESs), represented by their corresponding boolean graphs encoded implicitly in an OPEN/CÆSAR-style. The library currently contains four BES resolution algorithms, each one implementing a different strategy, and also allows to generate *diagnostics*, i.e., portions of the BES explaining the truth value of a variable. CÆSAR\_SOLVE has been used — in conjunction with OPEN/CÆSAR — for building two on-the-fly verification tools: the equivalence checker BISIMULATOR and the model checker EVALUATOR.

The paper is organized as follows. Section 2 recalls the BES definition and briefly describes the four on-the-fly resolution algorithms. Section 3 presents the principles of the generic library CÆSAR\_SOLVE and its applications to equivalence checking and  $\mu$ -calculus model checking. Section 4 concludes and indicates directions for future work.

## 2 Boolean equation systems

A boolean equation system (BES) consists of several blocks of equations having boolean variables in their left-hand sides and disjunctive or conjunctive boolean formulas in their right-hand sides (see Figure 1). Each block of n equations denotes either a minimal ( $\mu$ ) or maximal ( $\nu$ ) fixed point solution of its associated functional defined over **Bool**<sup>n</sup>. Here we consider *alternation-free* BESs (i.e., without cyclic dependencies between blocks), which can be solved in linear time and thus are of practical interest.



Figure 1: An alternation-free boolean equation system

The *local* (or *on-the-fly*) resolution of a BES consists in computing the value of a given variable, without necessarily solving the whole system. A useful local resolution scheme for alternation-free BESs can be obtained by associating a resolution routine to each block of the BES; a routine is invoked whenever the value of a variable defined in its corresponding block is needed. This scheme yields simple algorithms, which must handle a single type of fixed point equations, and also allows to optimize the resolution of each block independently, according to its particular shape.

We developed the resolution algorithms by representing BESs as *boolean graphs* [1], which provide an intuitive way of reasoning about dependencies between boolean variables. All algorithms are based upon the same principle: forward exploration of the boolean graph starting at the variable of interest, and backward propagation (i.e., substitution) of variables whose values have been established. During the computation, the algorithms also gather information for constructing diagnostics (portions of the boolean graph illustrating the value of a variable) after termination of the resolution, using the approach proposed in [7]. We developed four local resolution algorithms [9]: A1 is depth-first search (DFS)-based and currently serves as engine in the EVALUATOR model checker for regular alternation-free  $\mu$ -calculus [10]; A2 is breadth-first search (BFS)-based and yields low-depth diagnostics; A3 and A4 are both DFS-based and exhibit reduced memory consumption for acyclic and disjunctive/conjunctive boolean graphs, respectively. A3 is currently used within CADP for checking  $\mu$ -calculus properties on large execution traces [8].

## 3 On-the-fly verification

Alternation-free BESs allow to encode various verification problems on LTSs, such as equivalence checking, i.e., comparison of two LTSs modulo strong and weak equivalences [2] and model checking, i.e., evaluation of alternation-free  $\mu$ -calculus formulas on an LTS [1]. This motivated the construction of a generic library within CADP, called CÆSAR\_SOLVE, which implements the four on-the-fly resolution algorithms for alternation-free BESs (see Section 2) and can be applied for building different verification tools. Genericity is achieved by encoding BESs and diagnostics as boolean graphs, through a precise API inspired by the OPEN/CÆSAR [5] API for representing implicit LTSs. The basic idea is to encode each equation block of a BES by the successor function of its corresponding boolean graph; after resolution of a boolean variable, the library can also provide a diagnostic (boolean subgraph rooted at the variable) by giving its successor function.

To use the CÆSAR\_SOLVE library, the designer of a verification tool must provide: (1) an implementation in C of the boolean variables as pointers to (application-dependent) memory areas of fixed size, equipped with comparison and hashing functions; (2) an iterator function which enumerates the successors of a given boolean variable; (3) if needed, a function which traverses the boolean subgraph provided as diagnostic in order to interpret it in terms of the application. So far we used CÆSAR\_SOLVE as engine for two on-the-fly verification tools with diagnostic developed within CADP (see Figure 2): the equivalence checker BISIMULATOR, which handles strong, branching, observational, safety, and  $\tau^*.a$  equivalence; and the model checker EVALUATOR, which handles alternation-free  $\mu$ -calculus formulas extended with regular expressions and data parameters. Both tools translate their verification problems into BES resolution and interpret diagnostics by traversing the corresponding boolean subgraphs using the successor function given as output by CÆSAR\_SOLVE.

We also identified several particular cases of practical interest where the memory-efficient algorithms A3 and A4 can be used [9]. For equivalence checking, A3 can be applied when one of the LTSs is acyclic (e.g., when checking the inclusion of an execution trace in the LTS of a specification) and A4 can be applied when one LTS is deterministic (e.g., when comparing a protocol LTS with its deterministic service LTS). For model checking, A3 can be applied when the LTS is acyclic (e.g., when checking formulas on execution traces) and A4 can be applied when the LTS is acyclic (e.g., when checking formulas on execution traces) and A4 can be applied when the formulas are expressed using the operators of ACTL [3] and PDL [4].



Figure 2: Use of OPEN/CÆSAR and CÆSAR\_SOLVE for on-the-fly verification

#### 4 Conclusion and future work

The CÆSAR\_SOLVE library is intended to provide a generic support for the construction of on-the-fly verification tools based upon BES resolution. Two directions for continuing this work seem promising. Firstly, to increase the flexibility of CÆSAR\_SOLVE, we plan to extend it with new BES resolution algorithms, which either optimize particular cases (e.g., model checking of ACTL and PDL formulas on execution traces, which yields disjunctive/conjunctive BESs with low-breadth, acyclic boolean graphs), or experiment with various exploration strategies (e.g., combined DFS-BFS, random exploration, etc.). Secondly, we envisage new applications of CÆSAR\_SOLVE in the field of verification: partial-order reduction by detecting  $\tau$ -confluent transitions (a prototype tool has already been developed and used for compositional verification [11]); test generation and discrete controller synthesis, both of which could be rephrased in terms of diagnostic generation.

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